



INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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First Named Inventor	Rajski
Art Unit	243 211
Examiner Name	Phung M. Chung

U.S. PATENT DOCUMENTS

Copies of U.S. Patent documents do not need to be provided, unless requested by the Patent and Trademark Office. For patents, provide the patent number and the issue date. For published U.S. applications, provide the publication number and the publication date. For unpublished pending patent applications, provide the application number and the filing date.

Examiner's Initials*	Cite No. (optional)	Number	Publication Date	Name of Applicant or Pattee
/PC/		5,072,178	12/10/1991	Matsumoto
/PC/		5,642,362	6/24/1997	Savir
/PC/		5,717,701	2/10/1998	Angelotti et al.
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/PC/		6,590,929	7/8/2003	Williams
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FOREIGN PATENT DOCUMENTS

Examiner's Initials*	Cite No. (optional)	Country	Number	Publication Date	Name of Applicant or Pattee
/PC/		Japan	63-286780 (w/partial translation)	11/24/1988	Iwasaki et al.
/PC/		Japan	03-012573 (w/English abstract)	1/21/1991	Matsumoto
/PC/		Japan	05-249197 (w/English abstract)	9/28/1993	Ikenaga et al.
/PC/		Japan	07-174822 (w/partial translation)	7/14/1995	Kondou
/PC/		Japan	08-015382 (w/partial translation)	1/19/1996	Hiraide et al.
/PC/		Europe	0 549 949	3/11/1998	Ikenaga et al.
/PC/		Japan	11-264860 (w/partial translation)	9/28/1999	Kitamura

OTHER DOCUMENTS

Examiner's Initials*	Cite No. (optional)	Konemann et al., "Built-In Logic Block Observation Techniques," <i>IEEE Test Conference</i> , 6 pp. (1979).
/PC/		

EXAMINER SIGNATURE:	/Phung Chung/	DATE CONSIDERED:	05/14/2007
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* Examiner: Initial if reference considered, whether or not in conformance with MPEP 609. Draw line through cite if not in conformance and not considered. Include copy of this form with next communication to applicant.